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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/728,306	12/04/2003	Owais Jamil	TESSERA 3.0-241 DIV	9622
38091	7590	04/27/2006	EXAMINER	
TESSERA LERNER DAVID et al. 600 SOUTH AVENUE WEST WESTFIELD, NJ 07090			NGUYEN, DONGHAI D	
			ART UNIT	PAPER NUMBER
			3729	

DATE MAILED: 04/27/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.		Applicant(s)	
	10/728,306		JAMIL, OWAIS	
	Examiner		Art Unit	
	Donghai D. Nguyen		3729	

– The MAILING DATE of this communication appears on the cover sheet with the correspondence address –
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 27 February 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1,3,4 and 6-12 is/are pending in the application.
- 4a) Of the above claim(s) 8-12 is/are withdrawn from consideration.
- 5) ☒ Claim(s) 3,4,6 and 7 is/are allowed.
- 6) ☒ Claim(s) 1 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Response to Amendment

1. The amendment filed on 27 February 2006 has been considered and made of record.

Claim Objections

2. Claim 1 is objected to because of the following informalities: "blob or solder" (line 21) should be: --blob of solder--. Appropriate correction is required.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over US Patent 5,886,409 to Ishino et al in view of US Patent 5,404,637 to Kawakami.

Ishino et al disclose a method of making a microelectronic package, comprising:
providing dielectric layer (2/11 see Figs. 2 and 5a) having a top side and a bottom side; forming top conductive feature (4) on the top side of the dielectric layer and forming bottom conductive feature (7) on the bottom side the dielectric layer, said top and bottom conductive features being solder-wettable (see Col. 8, lines 25-29); juxtaposing the dielectric layer with least one microelectronic element (1) having contacts (8/9) and bonding the contacts microelectronic element with bottom conductive feature (7); forming via (17) through the dielectric layer either

before after said bonding step, the being aligned with the conductive feature and the bottom conductive feature (see Fig. 5e); and electrically interconnecting the top conductive feature and the bottom conductive feature through the vias depositing electrically-conductive material (18) the vias without seeding the vias; however, Ishino et al do not disclose the electrically-conductive material being a blob of solder. Kawakami teaches the step of interconnecting the top conductive feature (8/10) and the bottom conductive feature (9/11, see Fig. 1) through the vias (12) by depositing blob of solder (17) into the vias without seeding the vias for simplifying the process of manufacturing (see Col. 7, lines 8-13). Therefore it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the invention of Ishino et al by utilize the filling the blob of solder into the vias to electrically interconnect the top and bottom conductive features for simplifying the process of manufacturing to obtain the microelectronic package with low cost and environmental friendly.

Allowable Subject Matter

5. Claims 3, 4, 6 and 7 are allowed.

Response to Arguments

6. Applicant's arguments with respect to claim 1 have been considered but are moot in view of the new ground(s) of rejection.
7. This application contains claims 8-12 drawn to an invention nonelected without traverse in the response to Election Restriction filed on 6/7/2004. A complete reply to the final rejection

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must include cancellation of nonelected claims or other appropriate action (37 CFR 1.144) See MPEP § 821.01.

Conclusion

8. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Donghai D. Nguyen whose telephone number is (571)-272-4566. The examiner can normally be reached on Monday-Friday (9:00-6:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Peter D. Vo can be reached on (571)-272-4690. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DN
April 21, 2006


MINH TRINH
PRIMARY EXAMINER